Service-Instructions
Point-to-point with straight cut control
TNC 131



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1. Wiring Diagram

Subassemblies contained in the control and their connections.

TNC 131 consists of:

- power supply
- analog component board with inputs for measuring systems
- display board including keyboardsCPU board with V.24 interface
- input board
 output board
 terminal board
 interface board

1.1 Power supply

The power supply comprises:

- filter board
- voltage selector with built-in fuse
- safety transformer
- heat sink with +15 V regulator and two bridge rectifiers
- power supply board

Specifications:

voltage

selectable 100/120/140/200/220/240 V

voltage tolerance

+10%, -15%

frequence range

48 . . . 62 Hz

power consumption

66 W approx.

efficiency

 $\eta \ge 50\%$

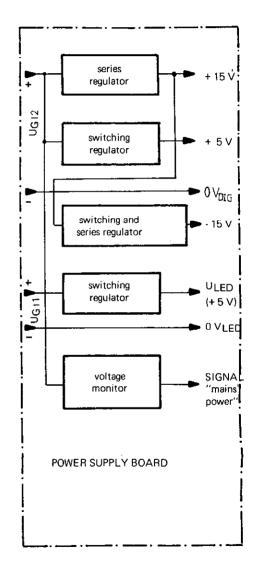
mains fuses

100/120/144 V : 1.0 A slow blow

200/220/240 V: 0.8 A slow blow

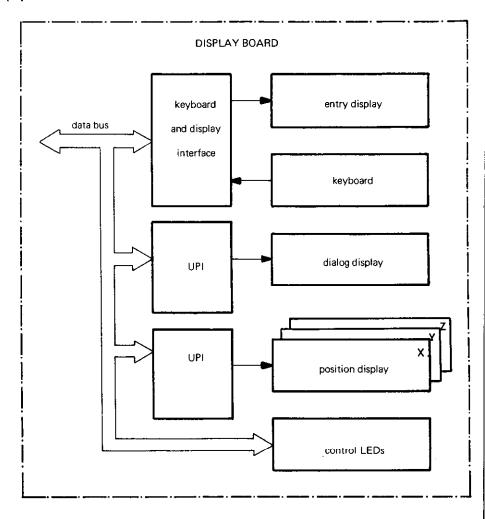
2. Block diagram

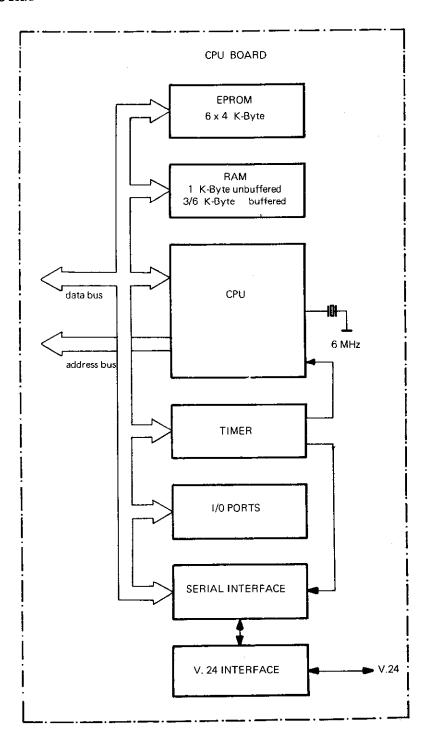
2.1 Power supply board



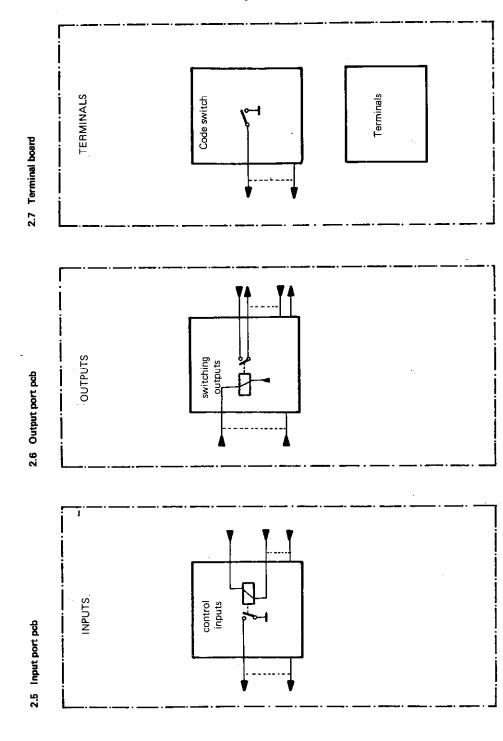
ANALOG COMPONENT BOARD EXE 2x RIDI TIMER TIMER with reference counting pulse evaluation dir. discri minator down up count count X, Y, Z measuring system monitor † U_{batt} ▶ battery monitor - U_{batt} ▶ Override Poti . Override Trigger 0V Override Feed Poti Feed 0∨ feed data bus DAC **%**≥ <u>+</u> 10 ∨ Analog output 0 V

2.3 Display board





- 5 -



To 2.1 Power supply board

The following voltages are generated:

+15 V

- 15 V (switching regulator with transverse choke) + 5 V (switching regulator with longitudinal choke)

+ 5 V (switching regulator with longitudinal choke)

(series regulator)

To 2.2 Analog board with inputs for measuring systems

.3 measuring system inputs (9 pole chassis mount) to connect the linear measuring systems or rotary encoders without built-in pulse shaper level (on TNC 131 S, TNC 131 ST).

Photocell signals 7...40 u A

Grating constant (pitch) 40 µm

.25 pole chassis mount for square wave signals U_{a1}, U_{a2}, U_{a0}, U_{aS} (on TNC 131 R, TNC 131 RT).

The analog board consists of:

- one each analog input part (2 x EXE with reference pulse evaluation for X, Y and Z axes — on TNC 131 S, TNC 131 ST).

-3 x square wave input for signals Ua1, Ua2, Ua0 and UaS with differential line receiver per RS-422 (on TNC 131 R, TNC 131 RT).

- one each: direction discriminator (RIDI) with counting direction switchover possibility (via code switch on Terminal board) for X, Y and Z.

- two timers to process the up-down counting pulses of X, Y and Z axes.

- battery monitor

- Override and feed trigger

- temperatur monitor

- a D/A-converter for the output of the analog output voltages of the programmed feeds of X, Y and Z by means of the sample-hold-amplifier levels.

To 2.3 Display board

The display board consists of:

- one keyboard and display interface to interrogate the keyboard and operate the entry display
- 1 UPI to operate the three position displays X, Y and Z
- one control-LED-output

To 2.4 CPU board

The CPU board consists of:

- the CPU (micro processor 8085)
- 6 EPROMs (8 x 4 K-Bits) for program storage
- 1 K-Byte RAM unbuffered (work storage)
- 3 K-Byte RAM (TNC 131 S, TNC 131 R)
- 6 K-Byte RAM (TNC 131 ST, TNC 131 RT)
- Storage for user program and for required data for program flow.
- one input/output port for control inputs and outputs
- one timer to generate time signals for CPU and serial interface
- one serial interface with line driver and line receiver for V.24 specification.

To 2.5 Input port PCB

The control inputs are connected via floating relays on the input board.

To 2.6 Output port PCB

The control outputs are supplied via floating relay contacts on the SE-output board.

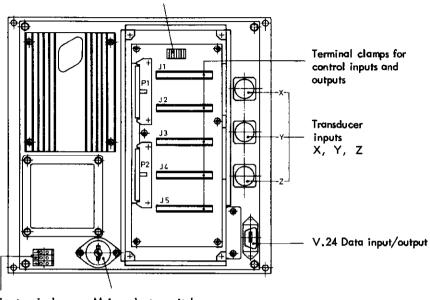
To 2.7 Terminal board

All control inputs and outputs are connected on terminals J1 to J5 of the terminal board. On the terminal board are also the code switches for ramp shape and length, as well as the display types.

3. Terminals

3.1 Open terminal box on rear of control





Mains terminals

Mains selector switch with fuse carrier

3.2 Interface

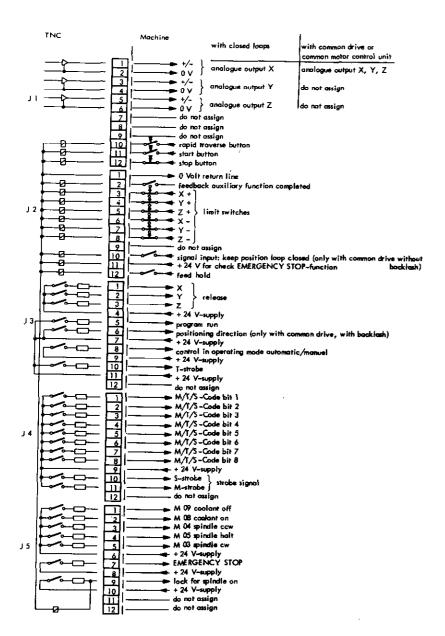
The interface between control and positioning system consists of the following signals:

```
.analog output X or X, Y, Z
.analog output Y with permanently closed positioning loop)
.analog output Z (with permanently closed positioning loop)
.relay output release X
.relay output release Y
.relay output release Z
.relay output positioning direction
.input limit switch X+
.input limit switch Y+
.input limit switch Z+
.input limit switch X-
.input limit switch Y-
.input limit switch Y-
.input limit switch Y-
.input limit switch Z-
```

Depending on the operating software (test program) the control works either with a position control circuit that is switched over to the individual axes, or with 3 permanently closed position control circuits.

3.2.1 Description (reference "Mounting Instructions and Interface TNC 131)

3.2.2 Layout of terminal box



4. Trouble shooting control/machine

It is of the greatest importance for the trouble shooting control/machine that the behaviour of the complete system is analyzed. Only then can the location of the error be pinpointed.

The following are aids for trouble shooting:

.a monitoring system within the control .a "BURN-IN-TEST PROGRAM"

.the following check list for trouble shooting control/machine

Check list for trouble shooting control/machine

Check correct setting of voltage and fuse in TNC:

4.1.1 Tests prior to machine switch-on:

for 100/120/140 V : fuse 1.0 A slow blow for 200/220/240 V : fuse 0.8 A slow blow	
4.1.1.1 Check measuring systems:	
The measuring systems (connector housing) must have a conductive connection to the machine grounding via the cable shield and the encoder head.	0
a) The connection is provided for X axis	0
b) The connection is provided for Y axis	0
c) The connection is provided for Z axis	0
The inner shield (pin 9) of the measuring system must not have connection to the connector housing.	0
a) No connection at X axis	0
b) No connection at Y axis	0
c) No connection at Z axis	0
All other connector pins of the measuring system must not have connection with the connector housing or with pin 9.	0
a) No connection at X axis	0
b) No connection at Y axis	0
c) No connection at Z axis	0

4.1.1.2 Test wiring on terminals

a)	Check that all cables on J1 – J5 are properly secured.	0
b)	Wiring in accordance with dwg.: "Terminal layout in terminal box of control (reference 3.2.2)	0
c)	The inputs of the servo amplifiers must be directly connected to the respective analog output of TNC 131. No resistors must be interconnected.	0

4.1.1.3 Test OV lines and their grounding

The OV return line of the external DC voltage must be grounded in the adapter cabinet.	0
The OV connections of the servo inputs must be grounded on the servo amplifiers. (connections on connector J1 Pin 2/4/6)	0

4.1.2 Tests after the switch-on of the adapter cabinet

Measure external DC voltage. Does it meet the requirements? + 30 V max. + 15 V min. time t	0
Check function of limit switch range and emergency stop limit switch.	0
4.1.3 Tests prior to switch-on of TNC 131	0
Remove battery from TNC and check battery voltage. Ubatt min = 4 V	0
Measure battery current: 1 _{batt} = 20 - 70 μA	0

4.1.4 Tests after the switch-on

4.1.4.1 Error indications

TNC 131 features an extensive monitoring system to prevent entry or operating errors and to diagnose technical defects in the machine and control system.

Monitored are:

.Programming and operating errors

e.g. error indications
BUTTON NON-FUNCTIONAL
TOOL NUMBER WRONG
WHOLE NUMBERS ONLY

Internal electronics of control

e.g. error indications
PROGRAM STORAGE DEFECT
TNC-OPERATING TEMP.EXEEDED
EXCHANGE BUFFER BATTERY

.Certain machine functions

e.g. error indications
GROSS POSITIONING ERROR
DEFECT ON X-MEAS.SYSTEM
RELAY EXT. DC VOLTAGE MISSING

Cancellation error of display

The control is inhibited while an error indication is active, i.e. to continue operations the error indication must be cleared.

The control TNC 131 distinguishes between two types of error indication:

minor errors

e.g. BUTTON NON-FUNCTIONAL

These errors are displayed without flashing and can be erased by simply pressing the E key.

.faults

such as faulty measuring systems, drives and defects on important electronic control functions.

With these major faults simultaneously the machine is stopped via the Emergency Stop contact of the control. These faults are indicated by a flashing display; the power supply must be switched off and the fault remedied.

Note:

In case of inquiries specify program number!

- 15 -

- 16 -

Error indication "EXCHANGE BUFFER BATTERY"

When "EXCHANGE BUFFER BATTERY" appears, new batteries should be installed (discharged battery holds the storage content for at least one week). The buffer batteries are behind the control panel in a removable battery carrier. Observe the correct polarity when changing battery (symbols are marked on the carrier).

Three "miniature cells" marked "LR6" of the "leakproof" type are required. Such batteries are commercially available.

We strongly recommend the use of Mallory Alkaline batteries type MN 1500.

If the buffer batteries are discharged or missing, program storage is supplied by the mains power. Operation may continue, however, a mains power failure will erase the stored program:

If during a battery change (discharged or missing battery) a mains power failure occures the machine parameters must be reprogrammed (reference paragraph B 3 Operating Instructions).

4.1.4.2 Test control functions	0
Switch on TNC 131 as set out in paragraph B 2 of Operating Instructions. Dialog displays in accordance with paragraph B 2?	0
If required, program machine parameters (see paragraph B 3 of Operating Instructions).	0
4.1.4.2.1 Test keyboard	0
Do the values entered via the decimal keyboard correspond with the entry display?	0
Are the values correctly transferred into the position display of the respective axis by pressing the axis key X, Y or Z and ENT?	0
4.1.4.2.2 Check the analog inputs and the counting function	0
Monitoring of measuring system: Is the error indication flashing:	0
"DEFECT ON X-MEAS.SYSTEM" after disconnecting the measuring system of X axis?	0
"DEFECT ON Y-MEAS.SYSTEM" after disconnecting the measuring system of Y axis?	0
"DEFECT ON Z-MEAS.SYSTEM" after disconnecting the measuring system of Z axis?	0
In the case that one axis does not count: Determine the source of error on measuring system/control by interchanging the connection cables of measuring systems and traversing the respective machine axes by hand.	0
4.1.4.2.3 Check the setting of the code switch (reference "Mounting Instructions and Interface")	0

5. Test Programs

The following program serves as an aid in trouble shooting

Program of BURN-IN-Test on complete control.

For the tests with testing programs the control must be completely disconnected from the machinel (Safety measures, e.g. EMERGENCY STOP, Stop etc. not active)

5.1 BURN-IN-Test TNC 131/135

The following applies to both TNC 131 and TNC 135:

Unit	Test program	IdentNo.
TNC 131 B, 500 blocks (4K-RAM) TNC 131 B, 1000 blocks (7K-RAM)	BURN-IN TNC 131 4K BURN-IN TNC 131 7K	212 920 01 212 935 01
TNC 135, 500 blocks (4K-RAM) TNC 135, 1000 blocks (7K-RAM)	BURN-IN TNC 135 4K BURN-IN TNC 135 7K	212 919 01 212 93 4 01

5.1.1 Required equipment or test fixtures

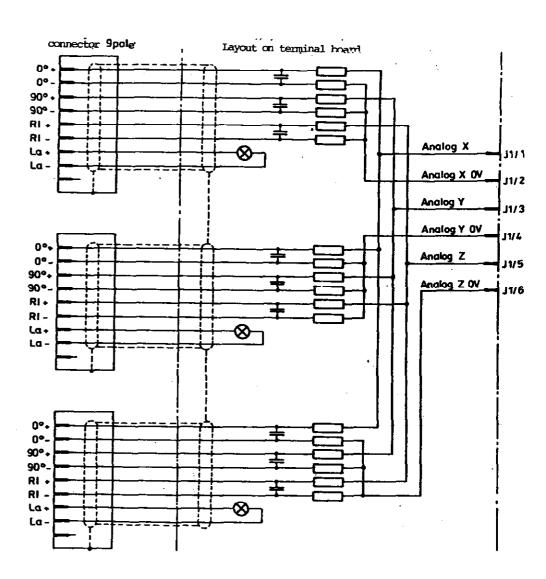
.ME 101/102 with data transm. cable

.Cassette"BURN-IN TNC K"

."Test connector for control inputs and outputs and analog input" (picture 1 and 2)

.Test connector RS-232C (picture 3)

Pict.l Test connector for analog input



cable LiYCY 4x2x0,14

resistors R = 18x130 kOhm

capacitors C = 9x1000 pF

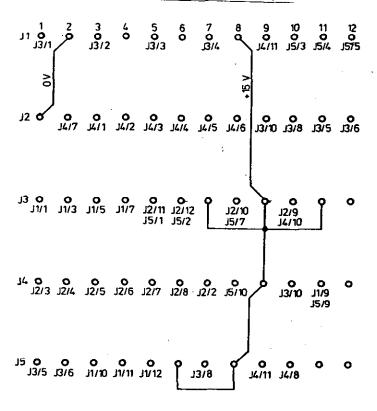
Id.Nr. 363 NE 200 854 01

Id.Nr. 410 NE 200 909 47

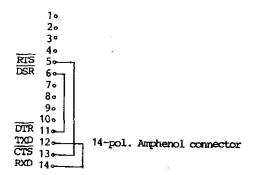
Id.Nr. 471 NE 200 751 35

F21 BA 212 281

Pict.2 Test connector for control inputs and outputs

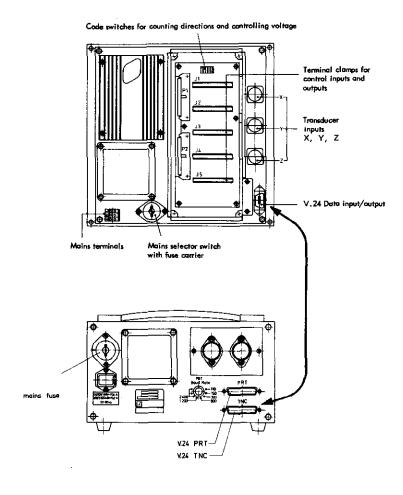


Pict.3 Test connector V.24-interface (ext.data inputs and outputs)

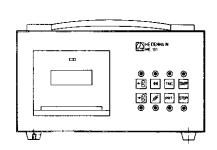


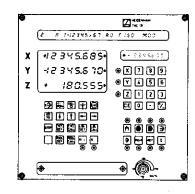
5.1.2 Preparation

- 1. Switch off power supply!
- 2. Remove TNC
- 3. Remove 4 mounting screws of terminal box on rear of control and take off cover.
- 4. Disconnect terminals J1-J5 for ext. connections.
- 5. Write down switch position of DIL switches!
- 6. Set DIL switches 1-8 upward
- 7. Connect "test connector J1-J5 with analog input" on terminals J1-J5 and analog inputs X, Y and Z. (Observe marking!)
- 8. Connect ME-TNC.



5.1.3 Testing sequence





1. Switch on ME:
all LEDs of ME light up;
all LEDs turn off after abt, 3 sec, except LED STOP.

2. Press TNC keys (EXT) and (CL) simultaneously

Turn on power supply for TNC:
 In dialog display: TNC 131: 32 alpha numeric digits

External Data entry appears

4. Data transfer ME - TNC

a) press ME key 🔤

b) press ME key 🗐

c) press ME key 🚾

d) press ME key 🚟

The program is re-recorded in the TNC.

This procedure lasts abt. 1 minute at 4K and abt. 2 minutes at 7K.

After the test program is re-recorded in the RAM, the data is checked (check-sum-test).
 If data transfer is wrong the error indication 00-FFFF starts flashing in the entry display.

- After the transmission of the data disconnect the data transm. cable on TNC and connect test connecter V.24, KEYBOARD OVERRIDE TEST appears in the dialog display.
- 7. Override Test

With the override potentiometer check on TNC whether the value from 0 to 145 can be controlled and then set it to exactly 100 ± 1 (visible in the entry display). (The marking on the knob must match the marking on the front panel exactly; if necessary the knob must be readjusted.

8. Keyboard test

Keyboard 1

Start with

🗵 then 7-8-9

▼ then 4-5-6

2 then 1-2-3

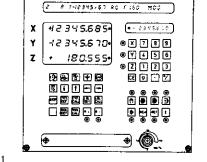
© then 0-.-±

Keyboard 2

Start with then sequence as under keyboard 1

Keyboard 3

Start with manual | then sequence as under keyboard 1



Z recens

If all keys are o.k., the dialog display will display for each activated key "*".

A keyboard error (defective key code, wrong sequence) effects a "?" in the dialog display. After abt. 3 sec.

"KEYBOARD OVERRIDE TEST" appears in the dialog display.

The keyboard test can be started again.

After a faultless keyboard test "BURN IN TIME CLEAR? WITH DIL SWITCH" appears in dialog display.

9. Set-back of BURN IN TIME and test DIL switch

The set-back of the BURN IN TIME should be done once after the read-in of the test program, after the keyboard test was completed. For the set-back of this time display the DIL switches on the terminal board (rear of control) in the sequence 1 to 8 one after another should be set from the "upward" position "downward" (position "down" on slide switch equals "open" on switches with rockers). For each switch max. 10 sec. time are provided. The malfunction of the switch effects a flashing error indication: EE — EEE.E in the entry display.

In this case the test program must be entered again. The entered program is cleared with the key. The dialog display will display: POWER FAILURE

10. Automatic test flow

After the BURN IN time set to 000.0 the cyclic BURN IN test starts automatically. The entry display will display the test status and the BURN IN time.

Example: Entry display

18 —

0.48.2

test status

BURN IN TIME in hours

(error code)

display step 0.1 hrs.

One test cycle lasts abt. 65 sec. at 4K (abt. 6 min. at 7K). The entry display starts flashing when an error is recognized. The BURN IN time stops and the error code is displayed.

After the BURN IN test the DIL switches must be set in the original position. Disconnect test connectors J1 — J5 and analog inputs, as well as V.24 test connector.

5.1.4 Jump into control program

(Clear test program symbol)

By pressing the CL key a jump from the test program into the control program is effected.

5.1.5 Test repeat in case of error

By pressing the | key a test repeat, starting with the DIL switch test (dialog display: BURN IN TIME CLEAR? WITH DIL SWITCH), can be activated.

By reactivating the DIL switches 1-8, as in paragr. 5.1.3-9, the BURN IN time can be reset to 0.

5.1.6 Interruption of BURN IN tests

The cyclic BURN IN test can be interrupted by switching off the power supply.

When the power supply is turned on again and the RAM test the power supply should not be interrupted (test status 11-17).

The error display

CC - XXX.X

indicates that the power supply was interrupted during the RAM tests. A new read-in of the test program is necessary.

5.1.7 Error code/Test statusBURN IN program TNC 131 4K

_	error code test status	description									
a)	00	Check sum RAM wrong after loading with ME (probable address error)									
	FF	Keyboard error									
	EE	DIL switch error									
	CC	Power failure during RAM test									
	UU	CPU test program not in EPROM 2									
		Start of cyclic tests									
b)	Test EPROM (tes	t check sum)									
	01	EPROM 1 CPU board Q 10 or address selection / interrogation									
	02	EPROM 2 " Q 6 "									
	03	EPROM 3 " Q 2 "									
	04	EPROM 4 " Q 9 "									
	05	EPROM 5 " Q 5 "									
	06	EPROM 6 " Q 1 "									
c)	Test RAM (4K – RAM)										
	11	RAM Q15/Q16 CPU board address range 6000H - 63FFH (1K)									
	12	RAM Q21/A22 " 6400H – 67FFH "									
	13	RAM Q27/Q28 " 6800H — 6BFFH "									
	14	RAM Q29/Q30 " 6C00H – 6FFFH "									
d)											
d)	Test CPU 8085 A										
d)	Test CPU 8085 A	CPU Q35 CPU board									
d) e)	18										
	18 Test TIMER, STA	CPU Q35 CPU board RT/STOP —Flip Flop Test status Test Timer and Start/Stop-flip-flop									
	18 Test TIMER, STA 20 21	CPU Q35 CPU board RT/STOP —Flip Flop Test status Test Timer and Start/Stop-flip-flop Error TIMER analog board Q26/9									
	18 Test TIMER, STA 20 21 22	CPU Q35 CPU board RT/STOP —Flip Flop Test status Test Timer and Start/Stop-flip-flop Error TIMER analog board Q26/9 " Q26/15									
	18 Test TIMER, STA 20 21 22 23	CPU Q35 CPU board IRT/STOP —Flip Flop Test status Test Timer and Start/Stop-flip-flop Error TIMER analog board Q26/9 " Q26/15 " " Q26/18									
	18 Test TIMER, STA 20 21 22 23 24	CPU Q35 CPU board IRT/STOP —Flip Flop Test status									
	18 Test TIMER, STA 20 21 22 23 24 25	CPU Q35 CPU board IRT/STOP —Flip Flop Test status Test Timer and Start/Stop-flip-flop Error TIMER analog board Q26/9 "									
	18 Test TIMER, STA 20 21 22 23 24 25 26	CPU Q35 CPU board IRT/STOP —Flip Flop Test status Test Timer and Start/Stop-flip-flop Error TIMER analog board Q26/9 " " Q26/15 " " Q26/18 " " Q22/18 " " Q22/15 " " Q22/18									
	18 Test TIMER, STA 20 21 22 23 24 25	CPU Q35 CPU board IRT/STOP —Flip Flop Test status Test Timer and Start/Stop-flip-flop Error TIMER analog board Q26/9 " Q26/15 " " Q26/18 " " Q22/9 " " Q22/15									

Test MONOFLOP, analog outputs X, Y, Z feed input INPUT/OUTPUT Ports and relays (with test connector picture 2)

9	Starti Off	ng cond	lition O	UTP 4	UT-PORTS Q7 on 5	Q20, Q26. Q	32 , Q44,	/4 on 0 Volt, n
0			CPU board	_	Q 51	CTIME PERI	OD)	•
- 1	Anal	og outp	ut X			31/1-33/1	Rel. 9	
32	•	•	¥		•	J1/3-J3/2	Rel.21	J 3/4 - J 1/7 connection on feed input
3	-		Z			J1/5~J3/3	Rel.10	
	Outp	./Inp.	7C/Bit	4	Q 20/Q4	J3/6-J2/12	Rel.11	Rel. 3
15	-			5	•	J3/5-J2/11	Rel.22	Rel.11
36		77		6		J3/8-J2/10	Rel.23	Rel. 4
17	-			7		J3/10-J2/ 9	Rel. 12	Rel.12
38	- .		•	8	•	J2/9-J1/·9	Rel. 1	Rel. 1
11			7D/Bit	1	Q26/Q7	J4/1-J2/3	Rel. 7	Rel.15
12	-	п	m	2	**	J4/2~J2/4	Rel. 8	Rel. 7
13	-	•		3	**	J4/3~J2/5	Rel.20	Rel.14
4	-		-	4		J4/4-J2/6	Pel.19	Pel. 6
15	-		- •	5		J4/5-J2/7	Pel.24	Rel.13
16	-	*	-	6	* .	J4/6~J2/B	Rel. 6	Pel. 5
17	"	*		7		J4/7-J2/2	Rel.18	Rel. 8
18	"	**		8	•	J4/8-J5/10	Rel. 5	Rel.16
1	"	63	7E/Bit	1	Q32/Q4	J5/5-J1/12	Pel.14	Pel.10
2	"	n		2		J5/4-J1/11	Rel. 3	Rel. 2
3	"	*	**	3	n	J5/3-J1/10	Rel.15	Rel. 9
4	"	11	• #	4	••	J5/2-J2/12	Rel. 4	
5	"	Ħ	11	5	•	J5/1-J2/11	Rel.16	
6	-	•		6	**	J5/7~J2/10	Rel. 2	
7	"	n	**	7		J4/10-J2/9	Rel.17	
8 -	47	•	19	8	H	J4/11-J1/9	Rel.13	
9	*	n	7D/all	Bi	t + 7E/al	l Bit		

Note: Relay test 1,1 ms for 2 relays in series (max. pick-up time 0.4 ms max. bounce time 0.15 ms)

0	error RIS-CIS Q36 CPU board
51	error RS 232 data transmission TxD-RxD
2	error RS 232 parity
	error RS 232 overflow
4	error RS 232 stop bit
55	error DTR-DSR
est feed o	override, battery, temperature
6 6 .	Test override poti on 100 + 1%
57	battery
58	excessive temperature (no test with DIL switch position
	mination switching, RI - Start/Stop-Flip/Flop (4 K - RAM) splay via axis LED X, Y, 2
axis dis	mination switching, RI - Start/Stop-Flip/Flop (4 K - RAM)
axis dis	mination switching, RI - Start/Stop-Flip/Flop (4 K - RAM) splay via axis LED X, Y, 2
	mination switching, RI - Start/Stop-Flip/Flop (4 K - RAM) splay via axis LED X, Y, Z Test contamination trigger and 0° signals LED test condition R+/R- test all contamination triggers simultaneously
axis dis	Test contamination trigger and 0° signals LED test condition R+/R- test all contamination triggers simultaneousl no contamination, test track 0° negative
axis dis	mination switching, RI - Start/Stop-Flip/Flop (4 K - RAM) splay via axis LED X, Y, Z Test contamination trigger and 0° signals LED test condition R+/R- test all contamination triggers simultaneously
exis dis	Test contamination trigger and 0° signals LED test condition R+/R- test all contamination triggers simultaneousl no contamination, test track 0° negative
exis dis	Test contamination trigger and 0° signals LED test condition R+/R- test all contamination triggers simultaneously no contamination, test track 0° negative R+ no contamination, rest track 0° positive
axis dis	mination switching, RI - Start/Stop-Flip/Flop (4 K - RAM) splay via axis LED X, Y, 2 Test contamination trigger and 0° signals LED test condition R+/R- test all contamination triggers simultaneously R- no contamination, test track 0° negative R+ no contamination, rest track 0° positive Test 90°-Signal
axis dis	Test contamination trigger and 0° signals LED test condition R+/R- test all contamination triggers simultaneousl R- no contamination, test track 0° negative R+ no contamination, rest track 0° positive Test 90°-Signal R- no contamination test track 90° negative
axis dis	mination switching, RI - Start/Stop-Flip/Flop (4 K - RAM) splay via axis IED X, Y, Z Test contamination trigger and 0° signals LED test condition R+/R- test all contamination triggers simultaneously R- no contamination, test track 0° negative R+ no contamination, rest track 0° positive Test 90°-Signal R- no contamination test track 90° negative R+ no contamination test track 90° positive
axis dis	Test contamination trigger and 0° signals LED test condition R+/R- test all contamination triggers simultaneousl R- no contamination, test track 0° negative R+ no contamination, rest track 0° positive Test 90°-Signal R- no contamination test track 90° negative no contamination test track 90° negative Test Diode connection to U ₀
70 71	Test contamination trigger and 0° signals LED test condition R+/R- test all contamination triggers simultaneousl R- no contamination, test track 0° negative R+ no contamination, rest track 0° positive Test 90°-Signal R- no contamination test track 90° negative R+ no contamination test track 90° positive Test Diode connection to U ₀ R- test with test track 0°/90° negative test with test track 0°/90° positive
axis dis	Test contamination trigger and 0° signals LED test condition R+/R- test all contamination triggers simultaneousl R- no contamination, test track 0° negative R+ no contamination, rest track 0° positive Test 90°-Signal R- no contamination test track 90° negative R+ no contamination test track 90° positive Test Diode connection to U ₀ R- test with test track 0°/90° negative

5.1.7 Error code/Test statusBURN IN program TNC 131 **7**K

	error code test status	description
a)	00	Check sum RAM wrong after loading with ME (probable address error)
	FF	Keyboard error
	EE	DIL switch error
	CC	Power failure during RAM test
	UU	CPU test program not in EPROM 2
		Start of cyclic tests
b)	Test EPROM (tes	t check sum)
	01	EPROM 1 CPU board Q 10 or address selection + interrogation
	02	EPROM 2 " Q 6 "
	03	EPROM 3 " Q 2 "
	04	EPROM 4 " Q 9 "
	05 06	EPROM 5 " Q 5 " EPROM 6 " Q 1 "
c)	Test RAM (7K -	RAM)
	11	RAM Q15/Q16 CPU board address range 6000 H-63FFH (1K
	12	" Q 21/Q22 " " " 6400 H-67FFH "
	13	" Q27/Q28 " " " 6800 H-6BFFH "
	14	" Q29/Q30 " " " " 6C00 H-6FFFH "
	15	" Q33/Q34 " " " 7000 H-73FFh "
	16	" Q38/Q39 " " * " 7400 H-77FFH *
	17	" Q40/Q41 " " " 7800 H-7BFFH "
d)	Test CPU 8085 A	
	18	CPU Q35 CPU board

e) —	e)	Test TIMER, START/STOP Flip Flop							
	20	Test stat	us	Test Timer and Start/St	op-flip-flop				
	21	Error	TIMER	analog boa	rd Q26/9				
	22	**	"	-,,	Q26/15				
	23	"	"	"	Q26/18				
	24	<i>u</i> .		**	Q22/9				
	25	ii .	**		Q22/15				
	26		"	"	022/18				
	27	Error Sta	ert/Stop	"	Q 1/Q18				
	28	**		**	Q 1/Q18				

Test MONOFLOP, analog outputs X, Y, Z feed input INPUT/OUTPUT Ports and relays (with test connector picture 2)

29	Starti Off	ing cond	lition () -PORIS ()	UIT 4.	UT-PORTS Q7 on 5	Q20, Q26. Q Volt.	32, Q44,	4 on 0 Volt, relay
30			CPU boar		Q 51	CTIME PERI	OP)	
31	Anal	og outp	ut X			J1/1-J3/1	Rel. 9	
32	•		¥		•	J1/3~J3/2	Rel.21	J 3/4 - J 1/7 connection on feed input
33	-		z			J1/5~J3/3	Rel.10	
34	Outp	./Inp.	7C/Bit	4	Q 20/Q4	J3/6-J2/12	Rel.11	Rel. 3
35		*		5	-	J3/5-J2/11	Rel.22	Rel.11
36	-	**	. •	6	•	J3/8-J2/10	Rel.23	Rel. 4
37	-	-	•	7	. =	J3/10-J2/ 9	Rel.12	Rel.12
38	•		-	8	•	J2/9~J1/·9	Rel. 1	Rel. 1
41			7D/Bit	1	Q26/Q7	J4/1-J2/3	Rel. 7	Rel.15
42	-			2	-	J4/2-J2/4	Rel. 8	Rel. 7
43	-	•		3	-	J4/3~J2/5	Rel.20	Rel.14
44	-	•	•	4	*	J4/4-J2/6	Pel.19	Pel. 6
45	-			5		J4/5-J2/7	Pel.24	Rel.13
46	-			6	• •	J4/6-J2/8	Pel. 6	Pel. 5
47			**	7	*	J4/7-J2/2	Rel.18	Rel. 8
48	-	•		8	•	J4/8~J5/10	Rel. 5	Rel.16
51		65	7E/Bit	1	Q32/Q4	J5/5~J1/12	Pel.14	Pel.10
52	*	11	B	2		J5/4~J1/11	Rel. 3	Rel. 2
53	"	**	••	3	**	J5/3-J1/10	Rel.15	Rel. 9
54	"	**	· in	4	Ħ	J5/2-J2/12	Rel. 4	
55	"	•	**	5	n	J5/1-J2/11	Rel.16	:
6	*	•	" .	6	•	J5/7~J2/10	Rel. 2	
57	-	#	***	7	M	J4/10-J2/9	Rel.17	
8 - 8	. .	#	n	8	н	J4/11-J1/9	Rel.13	
59	"	**	7D/all	Bi	t + 7E/al	l Bit		

Note: Relay test 1,1 ms for 2 relays in series (max. pick-up time 0.4 ms, max. bounce time 0.15 ms)

Test V.24 Interface (external data input/output with test connector (pict. 2)

60 .	error RTS-CTS Q36 CPU board
61	error RS 232 data transmission TxD-RxD
62	error RS 232 parity
63	error RS 232 overflow
64 .	error RS 232 stop bit
65	error DTR-DSR

Test feed override, battery, temperature

66	Test override poti on 100 + 1%_		
67	battery		
68	excessive temperature (no test with DIL switch position A A A A A A A A A A A A A A A A A A A		

Test the contamination switching and RI inputs, Start/Stop-flip/flop (7 K RAM)

Error indication via additional LED's		
70 71	contamination s	witching 0 ⁰ signals witching 90 ⁰ signals
	LED	type of error
	X,Y,Z	axis display of errors
	R+	no trigger point for pos. diode voltage*
	R-	no trigger point for neg. diode voltage*
	R+/REF	trigger value for pos.diode volt. outside tolerance*
	R-/REF	trigger value for neg.diode volt. outside tolerance*
	R+/I	hysteresis with pos. diode voltage
	R-/I	hysteresis with neg. diode voltage
	* Polarity of d	icde voltages are referenced to U _O .
72	Test diode conn	ection to U _o
	TED	type of error
	X,Y,Z	axis display of errors
	R+	polarity error of diodes with anode on U
i	P-	polarity error of diodes with cathode on U
73	Reference input	, Start/Stop-flip/flop
	LED	type of error
	X,Y,Z	axis display of error
	R+	no trigger with rising input voltage
	R-	no trigger with falling input voltage
	R+/REF	pos. trigger outside tolerance
	R-/REF	neg. trigger outside tolerance
	R+/I	hystersis too large
	R-/I	hystersis too small
	R+/R-/I	no hysteresis
		İ

Test inputs of meas, systems, EXE, RIDI (7 K - RAM)

<u> </u>			
7 7	Test w	ith counting direction forward	
78	Test w	ith counting direction reverse	
	LED		
	XX6	axis display of errors	
	R+	test timer forward	
	R-	test timer reverse	
		Trigger 0°	
	[479]	1113351 0	
		Trigger 45°	
		Trigger 90°	
		Trigger 135°	
	∌	Trigger 180°	
	INCH	Trigger 225°	
		•	
	REF	Trigger 270°	
		Trigger 315°	

6. Exchange of TNC 131

6.1 General remarks

Do not engage or disengage connectors whilst unit is under power!

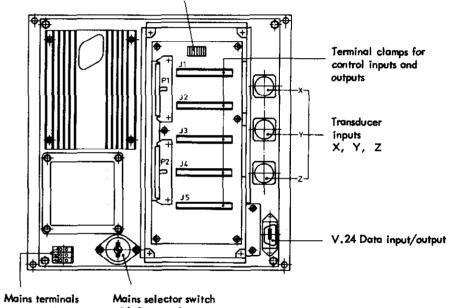
.NC machines also require those safety devices as used on manually operated machines (e.g. limit switches, "Emergency Stop" buttons etc.).

The proper function of these devices should be checked before commencing operation!

3. Terminals

3.1 Open terminal box on rear of control

Code switches for counting directions and controlling voltage



with fuse carrier

6.1.1 Work area requirements

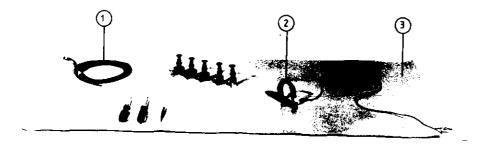
TNC 131 contains subassemblies with C-MOS elements. Although C-MOS circuits are provided with an input safety network to eliminate the build-up of a static charge-up, they must be handled with utmost care.

The following requirements in the work area must be met:

Prior to working with MOS components or with assemblies packed with MOS elements, all table coverings, all operated instruments or tools, as well as the work personnel, must be properly grounded.

A handy portable "MOS-HANDLING-SET" for field service is available, which is a MUST for every service kit.

- 1) a cable that equalizes potential differences between conductive work surface and ground
- 2) a wristband that provides an electrical connection between person and conductive work surface
- 3) a conductive work surface



6.2 Exchance of TNC 131

Caution:

Prior to exchange of the control the machine parameters must be written down.

1	Switch off power supply of system	
2	Remove mounting screws of control	
3	Take TNC carefully out of control cabinet	To the second se
4	Disconnect power supply on TNC; watch non-fused earthed conducter	uoo a
5	Disconnect measuring systems; mark X, Y, Z	fectiv
6	Remove cover from terminal box	of de
7	Disconnect J1 to J5, don't remove individual wires	Removal of defective control
8	Write down setting of code switches !!!	
9	Check correct voltage setting	
10	Check power fuse 100 140 V = 1,0 A; 200 240 V = 0,8 A	
11	Check program number]
12	Remove covers from cooling vents, if necessary, splashwater protection?	
13	Check code switch positions and set, if necessary] _
14	Connect terminal J1 to J5	contra
15	Mount cover of terminal box; watch cable run	new o
16	Connect measuring systems; note X, Y, Z	on of
17	Connect power supply, watch non-fused earthed conductor	Installation of new control
18	Insert and mount TNC carefully. Watch cables!	is in
19	Check machine parameters and reenter, if necessary]
20	Check TNC with machine]

6.3 Exchange of the service block TNC 131

1	Remove the 4 mounting screws from control housing (7 mm socket wrench) and lift front panel (Service-Block)
2	Disconnect the inputs of the measuring systems X, Y, Z (J1, J2, J3) on the analog board
3	Disconnect connecting cable P3 and P4 to the input port and output port PCBs on the CPU board
4	Disconnect power supply P2 on CPU board
5	Disconnect connecting cable RS-232C interface
6	Check program ident.number of new service block
7	Assemble in reverse sequence

6.4 Exchange boards 6.4.1 Exchange boards of service block TNC 131

1	Disconnect the battery connecting cable J3 on CPU board
2	Disconnect the connecting cable J1 to analog board
3	Disconnect the post connector P1 on CPU board
4	Remove the 5 mounting bolts of CPU board
5	Disconnect the connecting cable to override pot on analog board
6	Remove the 5 mounting bolts of analog board
7	Remove the 3 mounting bolts and the 2 mounting screws on display board
8	Disconnect the 5 keyboard connections P1 to P5 on display board
9	Assemble in reverse sequence

6.4.2 Exchange of boards in terminal box

1	Remove the 4 mounting screws of cover
2	Remove the 6 mounting screws of terminal board
3	Disconnect the 2 connecting cables P1 and P2 on the terminal board
4	Remove the 6 mounting bolts of the output port PCB
5	Disconnect the connecting cable P2 to CPU board
6	Remove the 6 mounting bolts of SE input board
7	Disconnect the connecting cable P2 to CPU board
8	Assemble in reverse sequence